

RESUMÉ—2-Page Professional MS Resumé Sample

YIJUN (YVONNE) WONG

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Provide preferred first name if different from given name. Include U.S. citizenship or permanent residency with "international-sounding" names.

OBJECTIVE

Advanced computer architecture, particularly in logic and architectural design. VLSI design, board-level design, and accompanying layout and interfacing.

EDUCATION

University of Wisconsin-Madison
M.S. Electrical Engineering, expected May 20XX

- GPA: 3.83/4.0
- Advisor: Professor Roberto Sangiovanni
- Thesis:

University of Wisconsin-Madison
B.S. Electrical Engineering, August 20XX

- Computer Engineering Option, GPA 3.51/4.0

EXPERIENCE

University of Wisconsin-Madison, Dept. of Electrical & Computer Engineering
Teaching Assistant, September 20XX–present

- Taught introductory electronic circuits to 60 general engineering students.
- Lectured, developed exams, and distributed grades.

Astronautics Corp. of America, Madison, WI
Project Engineer, January 20XX–August 20XX

- With team of seven, designed, built, and debugged the CPU of the Astronautics ZS-1 supercomputer.
- Designed the 300 MHzECL Master Clock Oscillator boards and clock distribution tree.
- Developed specifications of an I/O device for 100MB/s transfers; high-speed TTL and ECL signal behavior on transmission lines.

Certain Solutions (self-employed), Madison, WI
Engineering Consultant, March 20XX - present

- Solved noise-related printed circuit board problems for area firms
- Developed improved board layout design rules.

RESEARCH INTERESTS & PROJECTS

Design, fabrication and testing of real-time VLSI cache simulator. This is a c3000 transistor hardware monitor that computes would-be cache hit rates for a variety of cache types in parallel. The chip is currently being fabricated by MOSIS.

Designed a voice synthesis project in a computer projects course.
Received the Davis Award for Projects in Control Systems, October 20XX.

ECS TIPS

Trebuchet MS font style with 10 point font size, except for name (18 pt.) and section headers (12 pt.).

- ◆ Margins should be between $\frac{3}{4}$ " and 1".
- ◆ Allow enough "white space" for note-taking and easy visual scanning.

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NOTE: Two-page resumés are never printed back-to-back. Use two separate pieces of paper.

Use header or footer that includes name and page number for 2- to 3-page resumés.

Yijun (Yvonne) Wong
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SKILLS

Languages

- Proficient programming in C++.
- Significant experience with Pascal, FORTRAN, Modula II, BASIC, PL/I, and Clairon high-level languages.
- ZS-1, VAX, 68000, 80x86, Data General MV-10000, and other assembly languages.

Systems

- Ten years experience with UNIX.
- Two years experience as system manager, running Data General AOS/VS.
- Expert knowledge of MS-DOS and DR-DOS performance optimization/memory management.

CAD Tools

- Extensive experience with Valid Logic design tools for schematic capture, simulation, timing verification, packaging and placement.
- Extensive experience with the Magic VLSI design editor and IRSIM simulator. Experienced with Espresso, MISII, BDsyn, Cosmos, Cafe, Nova, Xdp, Spice, Micro-Logic II, Auto Sketch, MathCAD, and Minitab.

PUBLICATIONS & PRESENTATIONS

Lee, B., Y. Wong, "RACE: A hardware monitor for Real-Time Architectural Cache Evaluation Systems," submitted for publication.

Sangiovanni, R., B. Lee, Y. Wong, G.E. Dermer, "The ZS-1 Central Processor," presented at the Second International Conference on Architectural Support for Languages and Operating Systems (ASPROS II), IEEE/ACM (March), Palo Alto, California, 20XX.

REFERENCES

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When space permits, list references as last resumé section, instead of using an addendum page for references.